

IN THE CLAIMS

The pending claims are reproduced herein for the Examiner's convenience:

1. (Original) An electronic device having digital electronic communication capability, the electronic device comprising:
 - a data transceiver;
 - a clock driver with an enable input node;
 - a control circuit to drive the enable input node when the data transceiver is initialized; and
 - a clock receiver circuit having a clock detection circuit to detect the presence of an incoming clock signal;wherein the control circuit comprises an initialization circuit to initialize the data transceiver.
2. (Original) The electronic device of claim 1 wherein the data transceiver comprises:
 - a voltage mode output driver having an output node; and
 - a data receiver having an input node coupled to the output node of the voltage mode driver.
3. (Original) The electronic device of claim 1 wherein the data transceiver comprises:
 - a current mode output driver having a differential output node; and
 - a data receiver having a differential input node coupled to the differential output node of the current mode driver.
4. (Original) The electronic device of claim 1 wherein the initialization circuit comprises an impedance control circuit.

5. (Original) The electronic device of claim 4 wherein the control circuit is operative to enable the clock driver when the impedance control circuit has initialized an impedance of the data transceiver.
6. (Original) The electronic device of claim 5 wherein:
the data transceiver includes a voltage mode driver having an output impedance; and
the impedance initialized by the impedance control circuit is the output impedance of the voltage mode driver.
7. (Original) The electronic device of claim 5 wherein:
the data transceiver includes a current mode driver having at least one termination resistor; and
the impedance initialized by the impedance control circuit is the at least one termination resistor.
8. (Original) The electronic device of claim 1 wherein:
the data transceiver includes a variable current source circuit; and
the initialization circuit is operative to initialize the variable current source circuit.
9. (Original) The electronic device of claim 1 wherein:
the data transceiver includes a receiver circuit having a variable offset; and
the initialization circuit is operable to initialize the variable offset of the receiver circuit.